

ABSTRACT OF THE DISCLOSURE

One embodiment of the present disclosure provides a display system in which the display period of a conflict bit is skewed. Errors created by the skewing of the conflict bit may occur in one or more compensation bits. The compensation bits typically surround the conflict bit—and possibly the display periods or segments thereof of additional image data bits—in the bit sequence used to display an image data word. The compensation bits may be two segments corresponding to an image data bit, in which the errors imparted to the two segments may completely or partially cancel. The compensation bits may be two segments corresponding to the display periods of different image data bits. When the compensation bits correspond to different image bits additional segments of the different image data bits may occur elsewhere in the bit sequence and completely or partially cancel any errors.